

01-02-03

2829

3 Embarcadero Center
Suite 800San Francisco
California 94111T: 415-217-6000
F: 415-434-0646

skjerven morrill LLP

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JAN -6 2003

TECHNOLOGY CENTER 2800

December 30, 2002

Box Non-Fee Amendment
Commissioner For Patents
Washington, D.C. 20231

Re: Application No.: 09/780,208
 First Inventor: Hungyu H. Hou
 Group Art Unit: 2829
 Atty. Docket No.: M-9662 US
 Title: Voltage Detector Circuit With A Programmable Threshold Point
 Assignee: Fairchild Semiconductor Corporation

Confirmation No.: 6979
 Filing Date: February 9, 2001
 Examiner: Paresh H. Patel

Dear Sir:

Transmitted herewith are the following documents in the above-identified application:

- (1) Return Receipt Postcard;
- (2) This Transmittal Letter (in duplicate) (1 p.); and
- (3) Amendment and Response to Restriction Requirement 13 pgs.).

The fee has been calculated as shown below:

CLAIMS AS AMENDED

	Claims Remaining <u>After Amendment</u>	Highest No. Previously <u>Paid For</u>	Present Extra	Rate	Additional Fee
Total Claims	47	Minus	47	= 0 x \$18.00	\$ 0.00
Independent Claims	6	Minus	6	= 0 x \$84.00	\$ 0.00
<input type="checkbox"/> Fee of \$ _____ for the first filing of one or more multiple dependent claims					\$ _____
<input type="checkbox"/> Fee for Request for Extension of Time (_____ month(s))					\$ _____
<input type="checkbox"/> Fee for _____					\$ _____

Total additional fee for this Amendment:

- Please charge our Deposit Account No. 19-2386 in the amount of \$ 0.00
- Conditional Petition for Extension of Time: If an extension of time is required, the Commissioner is authorized to deduct the necessary fee from Deposit Account No. 19-2386.
- Also, charge any additional fees required and credit any overpayment to our Deposit Account No. 19-2386.

EXPRESS MAIL LABEL NO:

EV 259 162 304 US

Respectfully submitted,

N.R.H. Black
Agent
Reg. No. 43,320

910596 v1



#7/Electrical
Amndt B
RECEIVED 1-30-03
C. Moore

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application No.: 09/780,208

Confirmation No.: 6979

First Named Inventor: Hungyu H. Hou

JAN - 6 2003

Group Art Unit: 2829

Filing Date: February 28, 2001

Atty. Docket No.: M-9662 US

Examiner:

Pares H. Patel

Title: Voltage Detector Circuit With A Programmable Threshold Point

Assignee: Fairchild Semiconductor Corporation

San Francisco, California
December 30, 2002

BOX NON-FEE AMENDMENT
COMMISSIONER FOR PATENTS
Washington, D.C. 20231

AMENDMENT AND RESPONSE TO RESTRICTION REQUIREMENT

Dear Sir:

This communication is in response to the Office Action dated December 3, 2002, which has a statutorily shortened response period which ends January 3, 2003. Reconsideration is respectfully requested.

IN THE CLAIMS

The following is a clean version of the entire set of pending claims. In accordance with 37 CFR § 1.121(c)(1)(ii), the attached “*Version with markings to show changes made*” provides a marked-up version of the added claims showing the newly introduced changes. Please cancel Claim 36. Claim 48 has been added.

1. A voltage detector comprising: